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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/078,371	02/21/2002	Masamichi Tanaka	219783US2	5017
22850	7590 08/09/2004		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET			HUBER, PAUL W	
	RIA, VA 22314		ART UNIT	PAPER NUMBER
	*		2653	

DATE MAILED: 08/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.



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· ·	Application No.	Applicant(s)	X
	10/078,371	TANAKA, MASAMICHI	
Office Action Summary	Examiner	Art Unit	• • • •
	Paul Huber	2653	
The MAILING DATE of this communication appeared for Reply	pears on the cover sheet wi	th the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a naly within the statutory minimum of thirt will apply and will expire SIX (6) MON e, cause the application to become AB	eply be timely filed  y (30) days will be considered timely.  THS from the mailing date of this communication  ANDONED (35 U.S.C. § 133).	n.
Status			
1) Responsive to communication(s) filed on			
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ This	s action is non-final.		
3) Since this application is in condition for allowa	ince except for formal matt	ers, prosecution as to the merits i	s
closed in accordance with the practice under I	Ex parte Quayle, 1935 C.D	. 11, 453 O.G. 213.	
Disposition of Claims			
4)⊠ Claim(s) <u>1-3</u> is/are pending in the application.	:	,	
4a) Of the above claim(s) is/are withdra	wn from consideration.		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-3</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/o	or election requirement.		
Application Papers		,	
9) The specification is objected to by the Examine	er.		
10) The drawing(s) filed on is/are: a) acc		by the Examiner.	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the correc	tion is required if the drawing(	s) is objected to. See 37 CFR 1.121(	d).
11) The oath or declaration is objected to by the Ex	xaminer. Note the attached	Office Action or form PTO-152.	
Priority under 35 U.S.C. § 119			
12)⊠ Acknowledgment is made of a claim for foreign	nriority under 25 II C.C. &	110(a) (d) or (f)	
a)⊠ All b)□ Some * c)□ None of:	i priority under 35 0.5.C. §	119(a)-(u) or (1).	
1.⊠ Certified copies of the priority document	ts have been received.		
2. Certified copies of the priority document		oplication No.	
3. Copies of the certified copies of the prio		· ·	
application from the International Burea	u (PCT Rule 17.2(a)).		
* See the attached detailed Office action for a list	of the certified copies not	received.	
	, (		
Attachment(s)  1) Notice of References Cited (PTO-892)	A) [] Intonious S	ummary (PTO-413)	
2) Notice of References Cited (PTO-692)  Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s	)/Mail Date	
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>02212002</u> .	5)  Notice of In 6)  Other:	formal Patent Application (PTO-152) ·	
S. Patent and Trademark Office			

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The specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Nakano et al. (USP-5,566,142).

Nakano et al. discloses an optical pickup (see figures 3 & 4) comprising: a laser diode 4; and a high-frequency superposition module 14 that superposes a high-frequency current on a direct current of a laser diode 4, the module 14 including: an oscillating circuit 12, 13, 17 for feeding the high frequency to the laser diode 4, which includes at least an active element 13 and passive elements 12, 17; and a power supply 81 (+5V to 5) for feeding the direct current to the laser diode 4 which is also used as a power supply for the oscillating circuit, wherein the laser diode 4 is driven by the high-frequency superposition module 14.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nakano et al., as applied to claim 1 above, in further view of the admitted prior art of figures 6A & 6B as disclosed by the applicant in the specification.

Nakano et al. discloses the invention as claimed, but fails to specifically teach that the high-frequency superposition module 14 further includes an impedance matching circuit being provided between the oscillating circuit and the laser diode. However, the admitted prior art of figures 6A & 6B discloses an impedance matching circuit 10 as claimed, in the same field of endeavor, for the purpose of "preventing reflection" (page 3, line 5).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Nakano et al. such that the high-frequency superposition module 14 further includes an impedance matching



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circuit being provided between the oscillating circuit and the laser diode as claimed and as taught by the admitted prior art of figures 6A & 6B. A practitioner in the art would have been motivated to do this for the purpose of preventing reflection.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Doi and Sonoda each disclose a high-frequency superposition module for a laser diode.

Any inquiry concerning this communication should be directed to Paul Huber at telephone number 703-308-1549.

Paul Huber Primary Examiner Art Unit 2653

pwh August 4, 2004